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AVC Liquid Crystal Display GROUP
SHARP CORPORATION

SPECIFICATION

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APPLICABLE GROUP
AVC Liquid Crystal Display
Group

Group

TFT-LCD Module

MODEL

LQ150X1LGN2

CUSTOMERS APPROVAL
DATE
BY

PRESENTED

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AVC Liquid Crystal Display Group

SHARP Corporation



RECORDS OF REVISION

LQ150X1LGN2

SPEC No.	DATE	REVISED		SUMMARY	NOTE		
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Application

Global LCD Panel Exchange Center

This specification applies to the color 15.0 XGA TFT-LCD module LQ150X1LGN2.

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Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a 1024×RGB×768 dots panel with about 16 million colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	38 (Diagonal)	cm
	15.0 (Diagonal)	Inch
Active area	304.1 (H)×228.1 (V)	mm
Pixel format	1024 (H)×768 (V)	Pixel
	(1 pixel=R+G+B dots)	
Pixel pitch	0.297 (H)×0.297 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	$326.0(W) \times 252.0(H) \times 11.0(D)$	mm
Mass	1000 (MAX)	g
Surface treatment	Anti-glare and hard-coating 2H	
	(Haze value = 28)	

^{*1.}Note: excluding back light cables.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.



4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V DC power supply)

Using connectors : DF14H-20P-1.25H (Hirose Electric Co., Ltd.)

Corresponding connectors : DF14-20S-1.25C(Connector)

DF14-2628SCFA(Terminal)

 $Corresponding\ LVDS\ Transmitter\ \ \vdots\ THC63LVDM83R(Thine)\ or\ compatible$

Pin No.	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND		
4	GND		
5	RXIN0-	Receiver signal (-)	LVDS
6	RXIN0+	Receiver signal (+)	LVDS
7	GND		
8	RXIN1-	Receiver signal (-)	LVDS
9	RXIN1+	Receiver signal (+)	LVDS
10	GND		
11	RXIN2-	Receiver signal (-)	LVDS
12	RXIN2+	Receiver signal (+)	LVDS
13	GND		
14	RXCKIN-	Clock signal (-)	LVDS
15	RXCKIN+	Clock signal (+)	LVDS
16	GND		
17	RXIN3-	Receiver signal (-)	LVDS
18	RXIN3+	Receiver signal (+)	LVDS
19	GND		
20	LVDS_SET	LVDS_SET	[note1]

4-2 Data Mapping

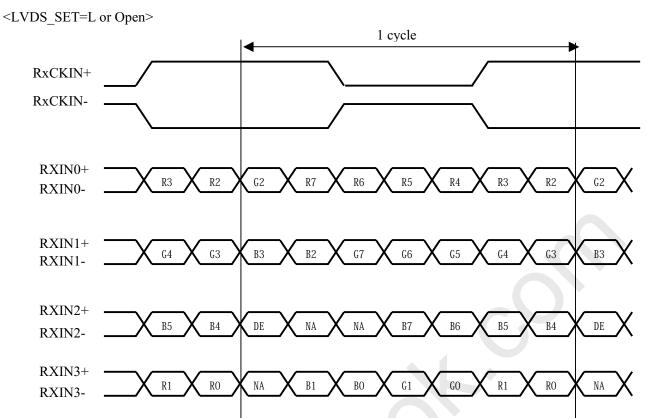
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1) 8 bit input

[note1] pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

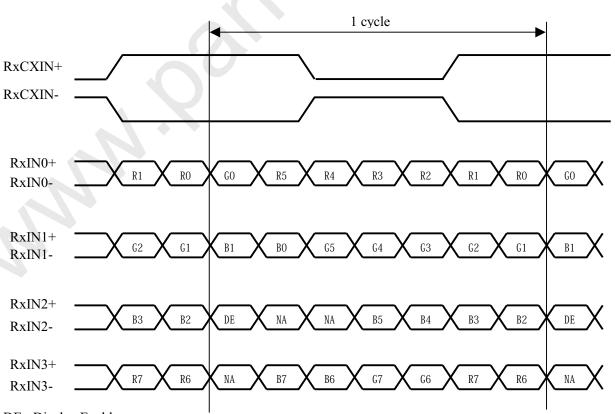
Tran	smitter	20pin L	VDS_SET						
Pin No	Data	=L (GND) or Open	=H (3.3V)						
51	TA0	R2	R0 (LSB)						
52	TA1	R3	R1						
54	TA2	R4	R2						
55	TA3	R5	R3						
56	TA4	R6	R4						
3	TA5	R7 (MSB)	R5						
4	TA6	G2	G0 (LSB)						
6	TB0	G3	G1						
7	TB1	G4	G2						
11	TB2	G5	G3						
12	TB3	G6	G4						
14	TB4	G7 (MSB)	G5						
15	TB5	B2	B0 (LSB)						
19	TB6	B3	B1						
20	TC0	B4	B2						
22	TC1	B5	В3						
23	TC2	B6	B4						
24	TC3	B7 (MSB)	B5						
27	TC4	(NA)	(NA)						
28	TC5	(NA)	(NA)						
30	TC6	DE	DE						
50	TD0	R0 (LSB)	R6						
2	TD1	R1	R7 (MSB)						
8	TD2	G0 (LSB)	G6						
10	TD3	G1	G7 (MSB)						
16	TD4	B0 (LSB)	B6						
18	TD5	B1	B7 (MSB)						
25	TD6	(NA)	(NA)						





DE : Display Enable NA : Not Available

<LVDS_SET =H>



DE : Display Enable NA : Not Available

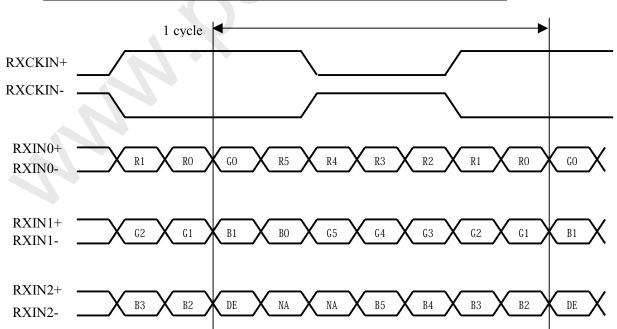


4-2 Data Mapping

2) 6 bit input

[note1] pin assignment with LVDS_SET pin (Thine:THC63LVDM83R)

Tran	smitter	20pin I	LVDS_SET
Pin No	Data	=L (GND) or Open	=H (3.3V)
51	TA0	R0 (LSB)	_
52	TA1	R1	_
54	TA2	R2	_
55	TA3	R3	_
56	TA4	R4	_
3	TA5	R5 (MSB)	_
4	TA6	G0 (LSB)	_
6	TB0	G1	_
7	TB1	G2	_
11	TB2	G3	_
12	TB3	G4	_
14	TB4	G5 (MSB)	_ (
15	TB5	B0 (LSB)	_
19	TB6	B1	
20	TC0	B2	
22	TC1	В3	_
23	TC2	B4	
24	TC3	B5 (MSB)	
27	TC4	(NA)	_
28	TC5	(NA)) –
30	TC6	DE	_
50	TD0	GND	_
2	TD1	GND	_
8	TD2	GND	_
10	TD3	GND	_
16	TD4	GND	_
18	TD5	GND	_
25	TD6	(NA)	_



DE : Display Enable NA : Not Available

**In case of supplying 6 bit signal, it is recommended to connect pin No.17(Rx3-) with VCC(3.3V), and No.18(Rx3+) with GND(0V).

(TFT-LCD side)

4-3 Interface block diagram

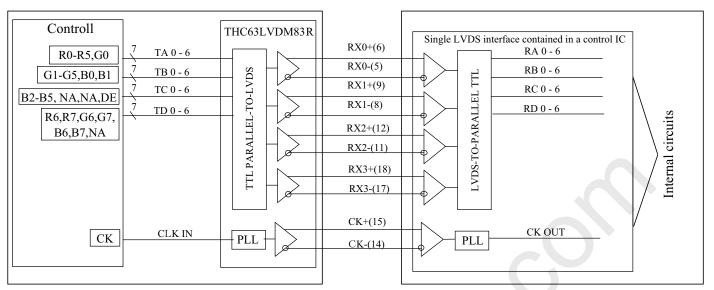
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(Computer Side)

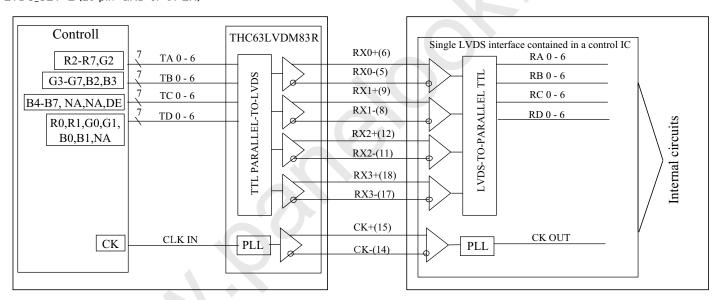
18Bit Mode

LVDS_SET=H (20 pin=3.3[V])



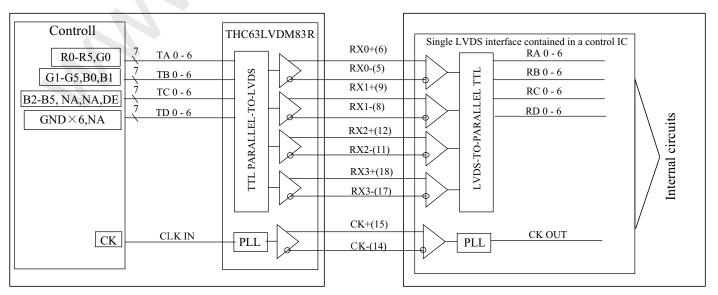
28Bit Mode

LVDS SET=L (20 pin=GND or OPEN)



36Bit Mode

LVDS_SET=L (20 pin=GND or OPEN)



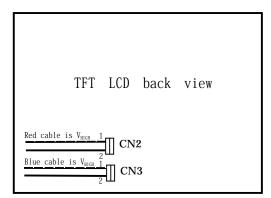


4-4 Backlight

CN 2, 3

The module-side connector : BHSR-02VS-1 (JST)
The user-side connector : SM02B-BHSS-1-TB (JST)

Pin no.	symbol	I/O	Func	tion
1	V_{HIGH}	I	Power supply for lamp	(High voltage side)
2	V_{LOW}	I	Power supply for lamp	(Low voltage side)



5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Supply voltage	Vcc	Ta=25°C	$0 \sim +4.0$	V	
Storage temperature	T_{STG}		$-25 \sim +60$	$^{\circ}$ C	[Note1]
Operating temperature (Ambient)	T _{OPA}		$0 \sim +50$	$^{\circ}$	

[Note1] Humidity: 95%RH Max. ($Ta \leq 40^{\circ}C$)

Maximum wet-bulb temperature at 39 $^{\circ}\text{C}~$ or less. ~ (Ta>40 $^{\circ}\text{C}~$)

No condensation.



6. Electrical Characteristics

6-1. TFT-LCD panel driving

Ta=25℃

	Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Vcc	Supply voltage		Vcc	+3.0	+3.3	+3.6	V	[Note2]
	Current dissipat	ion	Icc	_	425	700	mA	[Note3]
Permi	Permissive input ripple voltage			_		100	mVp-p	Vcc=+3.3V
Differ	ential input	High	V_{TH}	_	_	+100	mV	$V_{CM} = +1.2V$
thresh	old voltage	Low	V_{TL}	-100	_	_	mV	[Note1]
Input	current (High)		I_{OH}	_	_	±10	μΑ	V _I =2.4V, Vcc=3.6V
Input current (Low)			I_{OL}	_	_	±10	μΑ	V _I =0V, Vcc=3.6V
Ter	minal resistor		R_{T}	_	100	_	Ω	Differential input

[Note1] V_{CM} : Common mode voltage of LVDS driver.

[Note2]

1) On-off sequences of Vcc and data

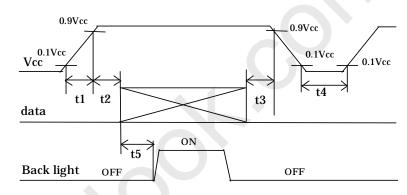
 $0 < t1 \le 10 \text{ms}$

 $0 < t2 \le 10 \text{ms}$

 $0 < t3 \le 1s$

1s≦t4

200ms≦t5



Power sequence for Backlight is not especially specified, however it is recommended to consider some timing difference between LVDS input and Backlight input as shown above.

If the Backlight lights on before LCD starting, or if the Backlight is kept on after LCD stopping, the screen may look white for a moment or abnormal image may be displayed.

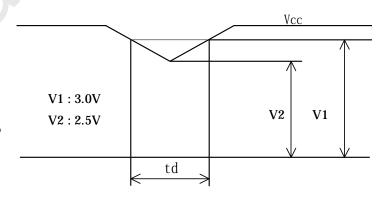
This is caused by variation in output signal from timing generator at LVDS input on or off. It does not cause the damage to the LCD module.

2) Dip conditions for supply voltage

1) $V2 \leq Vcc < V1$ $td \leq 10ms$

 $2) \quad Vcc \le V2$

Vcc-dip conditions should also follow the on-off conditions.



GS 0 GS,4

RGB RGB

[Note3] Typical current situation : 16-gray-bar pattern

Vcc=+3.3V, CK=65MHz

Horizontal period =20.7 us

Gray scale: GS(4n)

 $n=0\sim15$

The explanation of each gray scale, GS(4n), is described below section 8-2.

6-2. Backlight

The back light system is an edge-lighting type with 2 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

The value mentioned below is at the case of one CCFT.

CCFT Model Name: KTBE24MSTF-530.5KB270-Z-L (STANLEY ELECTRIC CO.,LTD)

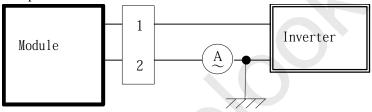
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Lamp current range	I_{L}	3.0	4.0	5.0	mArms	[Note1]
Lamp voltage	$V_{\rm L}$	_	1100	1250	Vrms	I _L =4.0 mArms Ta=25 °C 60kHz
Lamp power consumption	P_{L}	_	4.4	5.0	W	[Note2]
						IL= 4.0 mArms Ta= 25° C 60kHz
Lamp frequency	F_L	40	60	70	kHz	[Note3]
Kick-off voltage	Vs	_	_	1800	Vrms	Ta=25℃【Note4】
		_	_	2400	Vrms	Ta=0°C [Note4]
Lamp life time	$T_{\rm L}$	50,000	_	_	hour	[Note5]

[Note1] A lamp can be light in the range of lamp current shown above.

Maximum rating for current is measured by high frequency current measurement equipment connected to V_{LOW} at circuit showed below.

(Note: To keep enough kick-off voltage and necessary steady voltage for CCFT.)

Lamp frequency: 40∼70kHz Ambient temperature: $0\sim50^{\circ}$ C



- [Note2] Referential data per one CCFT by calculation ($IL \times VL$). The data don't include loss at inverter.
- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore, when designing an inverter, it is advisable to adopt a driving method in which two CCFTs are synchronized and driven at in-phase and anti-phase. The inverter should be placed away from the LCD module or shielded electromagnetically.
- [Note4] It is defined at 18pF for the ballast capacitor of a DC-AC inverter.

The kick-off voltage may rise up in the user set, please decide the open output voltage by checking not to occur lighting failure under operating state.

The open output voltage should be applied to the lamp for more than 1 second to startup. Or when the ambient luminance around the lamp is more than 1lux, it should be applied to the lamp for more than 100ms. Otherwise the lamp may not be turned on.

- [Note5] Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of Ta=25°C and IL=4.0 mA rms.
 - ① Brightness becomes 50% of the original value under standard condition.
 - ② Kick-off voltage at Ta=0°C exceeds 2400 V_{rms} value.

flow into one lamp when the other one is not in operation.

《Note》

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit

or lamp voltage waveform detection circuit, which should have individual control of each lamp. In case one circuit without such individual control is connected to more than two lamps, excessive current may

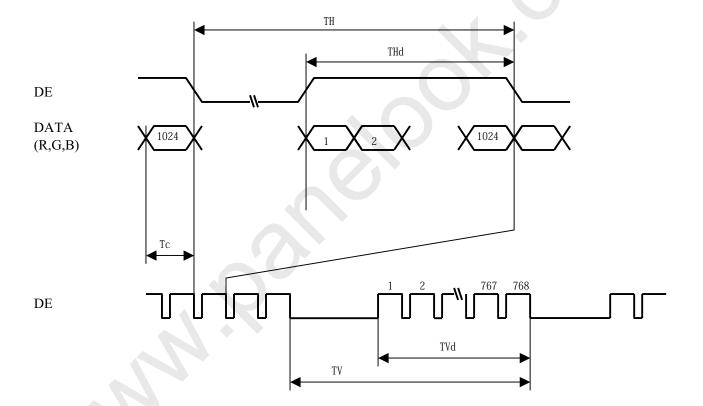
7. Timing characteristics of input signals

7-1. Timing characteristics

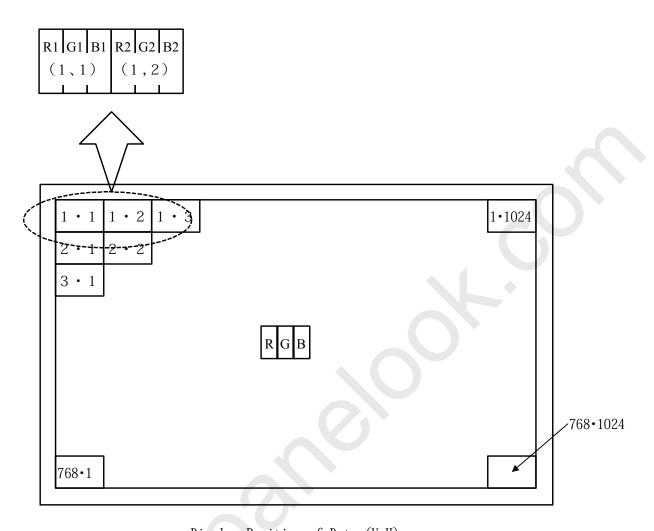
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	Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock signal	Frequency	1/Tc	50.0	65.0	80.0	MHz
ENAB signal	Horizontal period	TH	1056	1344	1720	clock
			16.0	20.7	23.4	μs
	Horizontal period (High)	THd	1024	1024	1024	clock
	Vertical period	TV	773	806	990	line
	Vertical period (High)	TVd	768	768	768	line

[Note] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2 Input Data Signals and Display Position on the screen



Display Position of Data (V,H)



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8	-1 8bi	t inpu	ıt																							
				Data signal																						
	Colors & Gray scale	Gray Scale	RO	R1	R2	R3	R4	R5	R6	R7	GO	G1	G2	G3	G4	G5	G6	G7	ВО	B1	B2	В3	B4	В5	В6	В7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1
В	Green	_	0	0	0	0	0	0	0	0	Х	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan	_	0	0	0	0	0	0	0	0	Х	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1
Color	Red	_	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
r	Magenta	_	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1
	Yellow	_	X	X	1	1	1	1	1	1	Х	Х	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	X	X	1	1	1	1	1	1	Х	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Û	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
iray	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scal	仓	\downarrow	V							4							↓									
le of	Û	\downarrow	\downarrow						V						↓											
Gray Scale of Red	Brighter	GS250	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gr	Û	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ay S	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Û	\downarrow					V							1	l							\				
of (Û	\downarrow					L C							1	<u>ا</u>							\	/			
iree	Brighter	GS250	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
n	Û	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ray :	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Scal	Û	\downarrow	V										1	\							V	/				
Gray Scale of Blue	Û	+	\downarrow						V											/						
Blue	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1
· ·	Û	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Х	Х	1	1	1	1	1	1

^{0:} Low level voltage,

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16-million-color display can be achieved on the screen.

^{1 :} High level voltage.

X :Don't care.



8-2 6bit input

8-	-2 6bit	input																		
		Data signal																		
	Colors &	Gray	D.O.	ъ.	D.O.	D.a	D.4	D.C		C1	G2	G2	64	0.5	D.O.	D.I	D2	D2	D.4	D.5
	Gray scale	Scale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	B1	B2	В3	B4	В5
Basic Color	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta		1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	→	V						↓					V						
le o	Û	\			,	\downarrow					1	1					`	L		
f Red	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	Û	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
ray (Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Scal	仓	→	Ψ					V					Ψ							
e of	Û	→	Ψ					↓					↓							
Gray Scale of Green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
n	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Blue	Û	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	Û	4	V					V					Ψ							
	Û	4	↓					\downarrow				\downarrow								
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1
	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 : Low level voltage,

1 : High level voltage.

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.



9. Optical Characteristics

 $Ta=25^{\circ}C$, Vcc=+3.3V

			1					23 0, 100 13.31	
Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	
Viewing	Vertical	θ11	CR≧5	40	55		Deg.	[Note1,4]	
angle		θ 12		70	80	_	Deg.		
range	Horizontal	θ 21, θ 22		70	80	_	Deg.		
	Vertical	θ11	CR≧10	30	45	_	Deg.		
		θ 12		45	55	_	Deg.		
	Horizontal	θ 21, θ 22		50	60		Deg.		
Contr	ast ratio	C R	$\theta = 0^{\circ}$	250	350	_		[Note2,4]	
Response Rise		γr			5	20	ms	[Note3,4]	
Time	Fall	γd			20	40	ms		
Chromaticity of		X		0.283	0.313	0.343		[Note4]	
White		у		0.299	0.329	0.359			
Chron	naticity of	X		0.551	0.581	0.611			
Red		у		0.292	0.322	0.352			
Chromaticity of		X	$\theta = 0^{\circ}$	0.277	0.307	0.337			
Green		у	$\theta = 0$	0.516	0.546	0.576			
Chromaticity of		X		0.121	0.151	0.181			
Blue		у		0.097	0.127	0.157			
Luminance of white		YL		200	260	_	cd/m ²	IL=4.0mA rms	
								fL=60kHz	
								[Note4]	
White U	Uniformity	δ w		_	_	1.25	_	[Note5]	

^{*}The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown

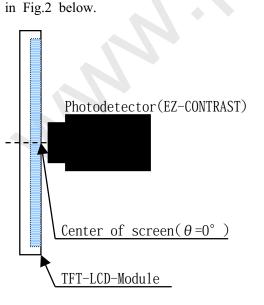
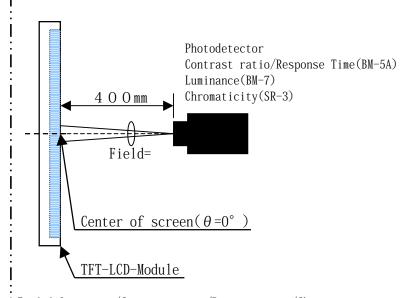


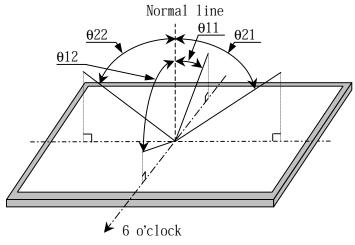
Fig2-1 Viewing angle measurement method



| Fig2-2 Luminance/Contrast ratio/Response time/Chromaticity measurement method

[Note1] Definitions of viewing angle range:

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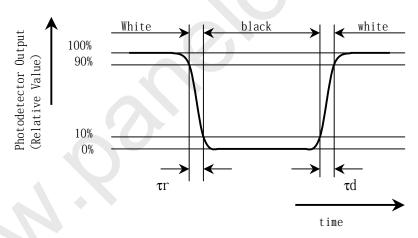
[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

Contrast Ratio (CR) =
$$\frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note3] Definition of response time:

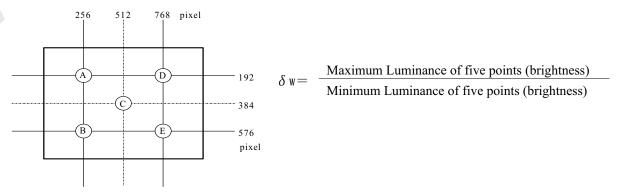
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements $(A \sim E)$.



10. Handling Precautions

- Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- Since the front polarize is easily damaged, pay attention not to scratch it. c)
- Since long contact with water may cause discoloration or spots, wipe off water drop immediately. d)
- When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth. e)
- Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with f) care.
- Since CMOS LSI is used in this module, take care of static electricity and take the human earth into g) consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- Observe all other precautionary requirements in handling components. j)
- When some pressure is added onto the module from rear side constantly, it causes display non-uniformity k) issue, functional defect, etc. So, please avoid such design.
- When handling LCD modules and assembling them into cabinets, please be noted that long-term 1) storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

11. Packing form

- a) Piling number of cartons: maximum 6 cartons
- b) Packing quantity in one carton: 10 modules
- c) Carton size : 408mm(W) $\times 340$ mm(H) $\times 398$ mm(D)
- d) Total mass of one carton filled with full modules: 11.2kg(typ.)
- e) Packing form is shown in Fig.3

Reliability test items

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12.	Reliability test items									
No	Test item	Conditions								
1	High temperature storage test	$Ta = 60^{\circ}C \qquad 240h$								
2	Low temperature storage test	$Ta = -25^{\circ}C \qquad 240h$								
3	High temperature	$Ta = 40^{\circ}C$; 95%RH 240h								
	& high humidity operation test	(No condensation)								
4	High temperature operation test	Ta = 50°C 240h								
		(The panel temp. must be less than 60°C)								
5	Low temperature operation test	$Ta = 0^{\circ}C$ 240H								
6	Vibration test	Waveform : Sine wave								
	(non- operating)	Frequency: 10~57Hz/Vibration width (one side): 0.075mm								
		: $58\sim500$ Hz/Gravity : 9.8 m/s ²								
		Sweep time: 11minutes								
		Test period: 3 hours								
		(1 hour for each direction of X,Y,Z)								
7	Shock test	Max. gravity: 490m/s ²								
	(non- operating)	Pulse width: 11ms, sine wave								
		Direction: $\pm X$, $\pm Y$, $\pm Z$,								
		once for each direction.								
8	Thermal shock test	Ta=-25°C \sim 60°C; 5 cycles								
	(Storage)	Test period: 10 hours (1 hour for each temperature)								
9	Altitude	$Ta=50^{\circ}C,70kPa,3,048m(10,000ft), t=24h$ (Operating)								
		$Ta=70^{\circ}C,12kPa,15,240m(50,000ft), t=24h$ (Storage)								

[Result Evaluation Criteria]

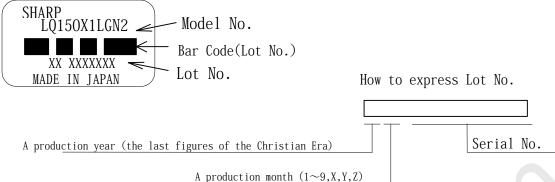
Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.



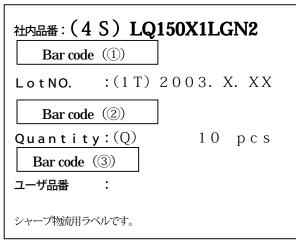
13. Others

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1) Lot No. and indication Bar Code Label:



2) Packing Label



- ① Model No. (LQ150X1LGN2)
- ② Lot No. (Date)
- 3 Quantity

- 3) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 4) Disassembling the module can cause permanent damage and should be strictly avoided.
- 5) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 6) The chemical compound which causes the destruction of ozone layer is not being used.
- 7) Warning of mercury and material information of LPG (Light Pipe Guide) are printed on the back of the module.

MATERIAL INFORMATION >PLASTIC LIGHT GUIDE:PMMA<

8)Cold cathode fluorescent lamp in LCD PANEL contains a small amount of mercury, Please follow local ordinances or regulations for disposal.

> COLD CATHODE FLUORESCENT LAMP IN LCD PANEL CONTAINS A SMALL AMOUNT OF MERCURY, PLEASE FOLLOW LOCAL ORDINANCES OR REGULATION FOR DISPOSAL 当該液晶ディスプレイパネルは蛍光管が組み込まれていますので、地方自冶体の条 例、または、規則に従って廃棄ください。

- 9) This specification document's Japanese language version is also available. Its Number (SPEC. No.) is LD-15307.
- 10) When any question or issue occurs, it shall be solved by mutual discussion.

14. Carton storage condition

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Temperature 0° C to 40° C Humidity 95%RH or less

Reference condition: 20°C to 35°C, 85%RH or less (summer)

5°C to 15°C, 85%RH or less (winter)

• the total storage time $(40^{\circ}\text{C},95\%\text{RH})$: 240H or less

Be sure to shelter a product from the direct sunlight. Sunlight

Atmosphere Harmful gas, such as acid and alkali which bites electronic components and/or

wires must not be detected.

Be sure to put cartons on palette or base, don't put it on floor, and store them with Notes

removing from wall

Please take care of ventilation in storehouse and around cartons, and control

changing temperature is within limits of natural environment

Storage period 1 year

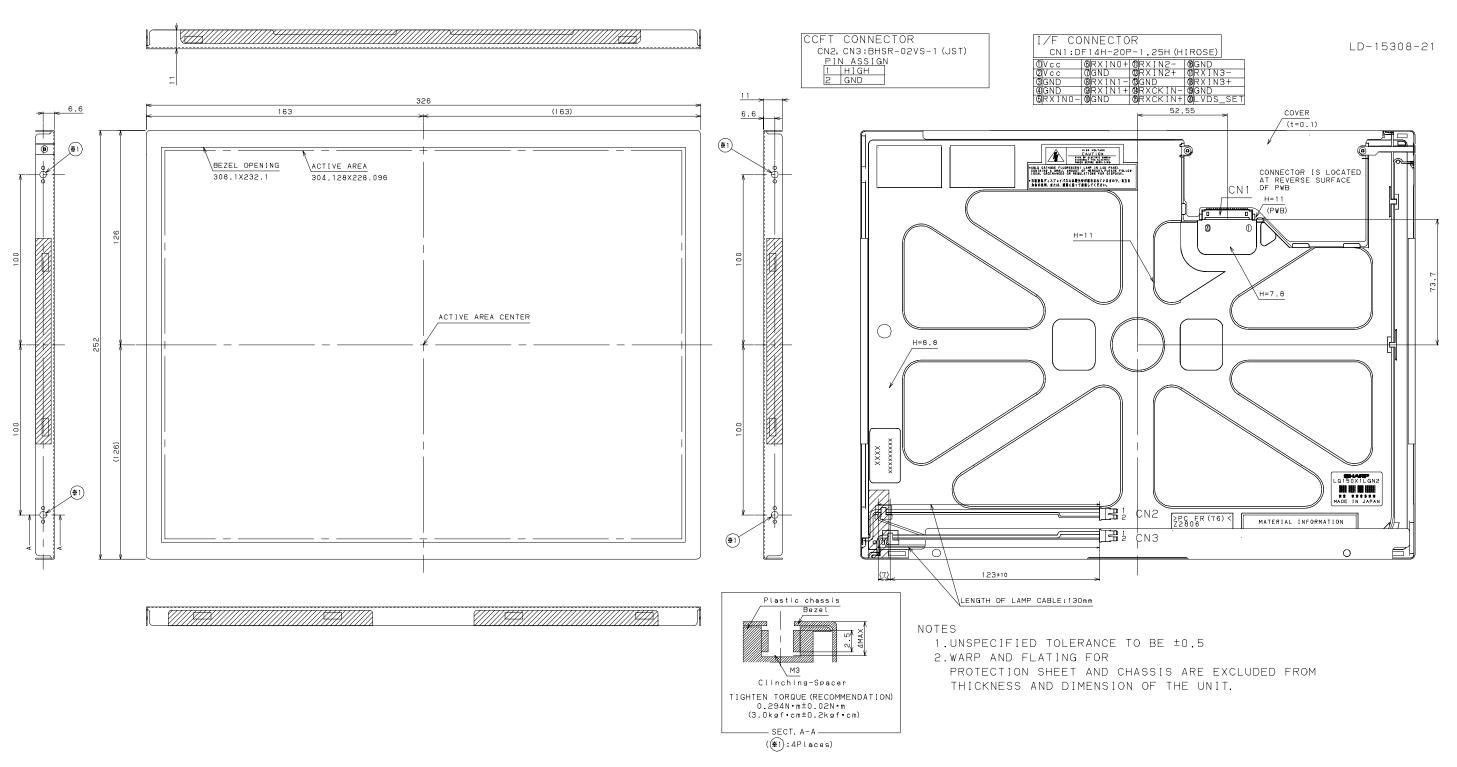


Fig. 1 LQ150X1LGN2 OUTLINE DIMENSIONS

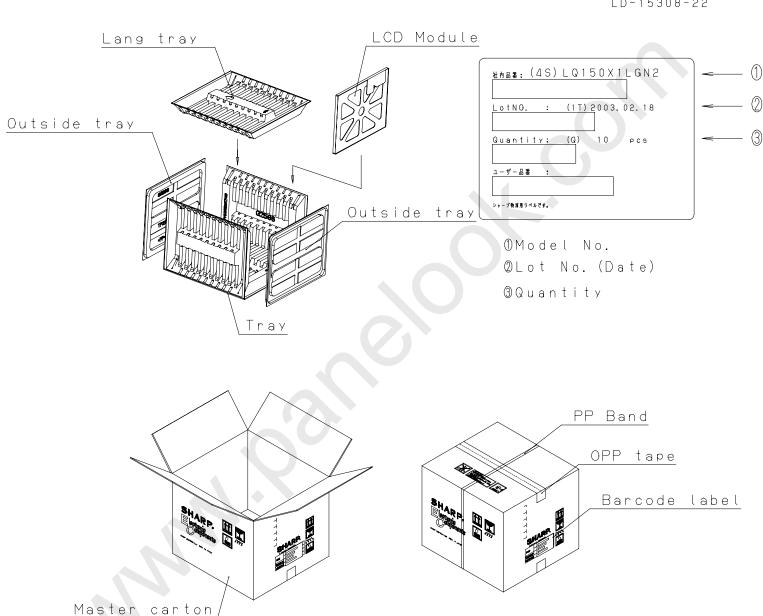


Fig. 3 Packing Form (LQ150X1LGN2)